LINEAR SYSTEMS

LSK170 ULTRA LOW NOISE SINGLE N-CHANNEL JFET



Linear Systems replaces discontinued Toshiba 2SK170 with LSK170

The 2SK170 / LSK170 is an Ultra Low Noise Single N-Channel JFET

	provide low noise at both high and low frequence		FEATURES							
with a narrow range of IDSS and low capacitance. The 2SK170 / LSK170's low noise to capacitance ratio and narrow range of low				TRA LO	$e_n = 0.9 nV/\sqrt{Hz}$					
	ovide solutions for low noise applications which	ню	GH BRE	BV _{GSS} = 40V max						
cannot tolerate high values of capacitance or wide ranges of IDSS The narrow ranges of IDSS binning with the 2SK170 / LSK170 promote ease of design tolerancing, particularly in low voltage applications. The 2SK170 / LSK170 is ideal for portable battery				GH GAI	Y _{fs} = 22mS (typ)					
				GH INPL	I _G = -500pA max					
				W CAP	22pF max					
	IMPROVED SECOND SOURCE REPLACEMENT FOR 2SK170									
operated applications, and features high BVDSS for maximum linear headroom in high transient program content amplifiers. The				ABSOLUTE MAXIMUM RATINGS ¹						
2SK170 / LSK	@ 25 °C (unless otherwise stated)									
front-end prea	stability that is highly desirable, particularly for implifiers.	Maximum Temperatures								
			Sto	orage Te	emperatur	e	-65 to +150 °C			
2SK170 / LSK170 Applications:			Op	erating	-55 to +135 °C					
Audio amplif	Ма	Maximum Power Dissipation								
operational a	Co	ntinuous	400mW							
v , ,	guitar pickups, effects pedals, microphones, audio mixer consoles, acoustic sensors, sonobuoys, hydrophones, chemical and radiation detectors, instrumentation					Maximum Currents				
						Gate Forward Current				
	ccelerometers, CT scanners input stages,		Maximum Voltages							
oscilloscope	oscilloscope input stages, electrometers and vibrations				Gate to Source					
detectors.				Gate to Drain V _{GDS} = 40						
Surface mour Improved pin Improved fund IF1331, and II	Available Packages: 2SK170 / LSK170 in TO-92 SK170 / LSK170 in SOT-23 2SK170 / LSK170 in SOT-23 2SK170 / LSK170 available as bare dieTOP Z BOTTOM VIEW TOP VIEW 									
ELECTRICAL CHARACTERISTICS @ 25 °C (unless otherwise stated) SYMBOL CHARACTERISTIC MIN TYP MAX UNITS CONDITIONS										
	Gate to Source Breakdown Voltage	40	116		V					
BV _{GSS}	Gate to Source Breakdown Voltage	40		2	V V	$V_{DS} = 0, I_D = 100 \mu A$				

BV _{GSS}	Gate to Source Breakdown Voltage		40			V	$V_{DS} = 0, I_{D} = 100 \mu A$	
V _{GS(OFF)}	Gate to Source Pinch-off Voltage		0.2		2	V	V _{DS} = 10V, I _D = 1nA	
V _{GS}	Gate to Source Operating Voltage			0.5		V	V _{DS} = 10V, I _D = 1mA	
I _{DSS}	Drain to Source Saturation Current	LSK170A	2.6		6.5	mA	V _{DG} = 10V, V _{GS} = 0	
		LSK170B	6		12			
		LSK170C	10		20			
l _G	Gate Operating Current				0.5	nA	$V_{DG} = 10V, I_{D} = 1mA$	
I _{GSS}	Gate to Source Leakage Current				1	nA	V _{DG} = 10V, V _{DS} = 0	
Y _{fss}	Full Conduction Transconductance			22		mS	V_{GD} = 10V, V_{GS} = 0, <i>f</i> = 1kHz	
Y _{fs}	Typical Conduction Transconductance			10		mS	V_{DG} = 15V, I_{D} = 1mA	
en	Noise Voltage			0.9	1.9	nV/√Hz	V _{DS} = 10V, I _D = 2mA, <i>f</i> = 1kHz, NBW = 1Hz	
en	Noise Voltage			2.5	4	nV/√Hz	V _{DS} = 10V, I _D = 2mA, <i>f</i> = 10Hz, NBW = 1Hz	
C _{ISS}	Common Source Input Capacitance			20		pF		
C _{RSS}	Common Source Reverse Transfer Cap.			5		pF	V _{DS} = 15V, I _D = 500µA	

1. Absolute maximum ratings are limiting values above which serviceability may be impaired.

Information furnished by Linear Integrated Systems is believed to be accurate and reliable. However, no responsibility is assumed for its use; nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Linear Integrated Systems.