LTCC
High Temperature Inductors
0.27x0.27 inch size

NASCENTechnology, Inc. has developed a series of high temperature, RoHS compliant inductors using low temperature co-fired ceramic (LTCC) ferrite usable to 300°C that feature low profile, rugged packaging, and self shielding characteristics. See individual data sheets for part dimensions.

Electrical Parameters at 25 °C unless otherwise noted

<table>
<thead>
<tr>
<th>Part No.</th>
<th>No Load Inductance†</th>
<th>100 mA Inductance</th>
<th>100 mA Tolerance</th>
<th>Nominal DC Resistance</th>
<th>Rated Current††</th>
<th>Saturation Current*</th>
</tr>
</thead>
<tbody>
<tr>
<td>85008</td>
<td>15 µH</td>
<td>12 µH</td>
<td>20%</td>
<td>0.40</td>
<td>350 mA</td>
<td>530 mA</td>
</tr>
<tr>
<td>85012</td>
<td>35 µH</td>
<td>29 µH</td>
<td>25%</td>
<td>0.60</td>
<td>530 mA</td>
<td>530 mA</td>
</tr>
<tr>
<td>85016</td>
<td>63 µH</td>
<td>48 µH</td>
<td>30%</td>
<td>0.80</td>
<td>250 mA</td>
<td>250 mA</td>
</tr>
<tr>
<td>85077</td>
<td>33 µH</td>
<td>30 µH</td>
<td>25%</td>
<td>0.38</td>
<td>250 mA</td>
<td>250 mA</td>
</tr>
</tbody>
</table>

†Tolerance for no load inductance is ±25 %
††Current will cause a 35 °C temperature rise over ambient (measured 20 to 55 °C)
* Saturation Current is the current that results in a 35% decrease in inductance over the 100 mA Inductance.
Thickness varies by part; the range is 0.032-0.100 in [0.8-2.54 mm]

Common termination options (please add option to the end of the part number when inquiring):
- A0 Thick film AgPt with side terminations, solderable
- B0 Leads, gold plated
- C0 Thick film Au, Au wirebonding
- D0 Au over Ni, solderable
- D1 Au over Ni, Al wirebonding

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This product is protected under US Patent #6054914, #6198374 and other international patents.
Specifications subject to change without notice. 03/13